

Wide-Band, High-Speed, Low-Offset, Low-Noise Rail-to-Rail Input/Output CMOS Operational Amplifier

■ GENERAL DESCRIPTION

The NJU77701 are Rail-to-Rail input/output CMOS single/dual/quad operational amplifiers. They feature wide-band, high-speed, low-input-offset voltage and low-noise.

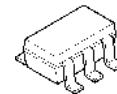
The NJU77701 has a high-speed characteristic of gain bandwidth 34MHz and slew rate 35V/μs. Moreover, the NJU77701 achieves temperature fluctuations low offset and low noise characteristic. 6nV/Hz typ. at f=1kHz.

Therefore, the NJU77701 devices easily offer various sensing applications that require high speed and accuracy. With their rail-to-rail output characteristic and 600-ohm load driving, these devices are able to secure wide dynamic range for various applications.

■ FEATURES

- Wide-Band 34MHz typ.
- High-SlewRate 35V/μs typ.
- Low Noise 6nV/ Hz typ. at f=1kHz
- Low Offset Voltage 1.5mV max.
- Low Offset Voltage Drift 3.0μV/°C max.
- Operating Current 3.8mA typ.
- Operating Voltage Range +2.4V to +5.5V
- Wide temperature range -40 to +125
- Rail-to-Rail Input/Output
- RF Immunity
- Package SOT-23-5

■ PACKAGE OUTLINE



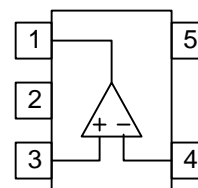
NJU77701F
(SOT-23-5)

■ APPLICATIONS

- Low noise signal processing
- ADC buffers
- DAC output amplifiers
- Current Sense amplifiers
- Radio systems

■ PIN CONFIGURATION

SOT-23-5
(Top View)



- 1. OUTPUT
- 2. V⁻
- 3. + INPUT
- 4. - INPUT
- 5. V⁺

NJU77701F

■ ABSOLUTE MAXIMUM RATINGS(Ta=25 unless otherwise noted.)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V ⁺ - V ⁻	+7	V
Differential Input Voltage ⁽¹⁾	V _{ID}	±7	V
Input Voltage ⁽²⁾	V _{IN}	V ⁻ - 0.3 to V ⁺ + 0.3V	V
Input Current	I _{IN}	10	mA
Output Terminal Input Voltage	V _O	V ⁻ - 0.3 to V ⁺ + 0.3V	V
Power Dissipation ⁽³⁾	P _D	480 (2-layer) / 640 (4-layer)	mW
Operating Temperature Range	T _{opr}	-40 to +125	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C

(1) Differential voltage is the voltage difference between +INPUT and -INPUT.

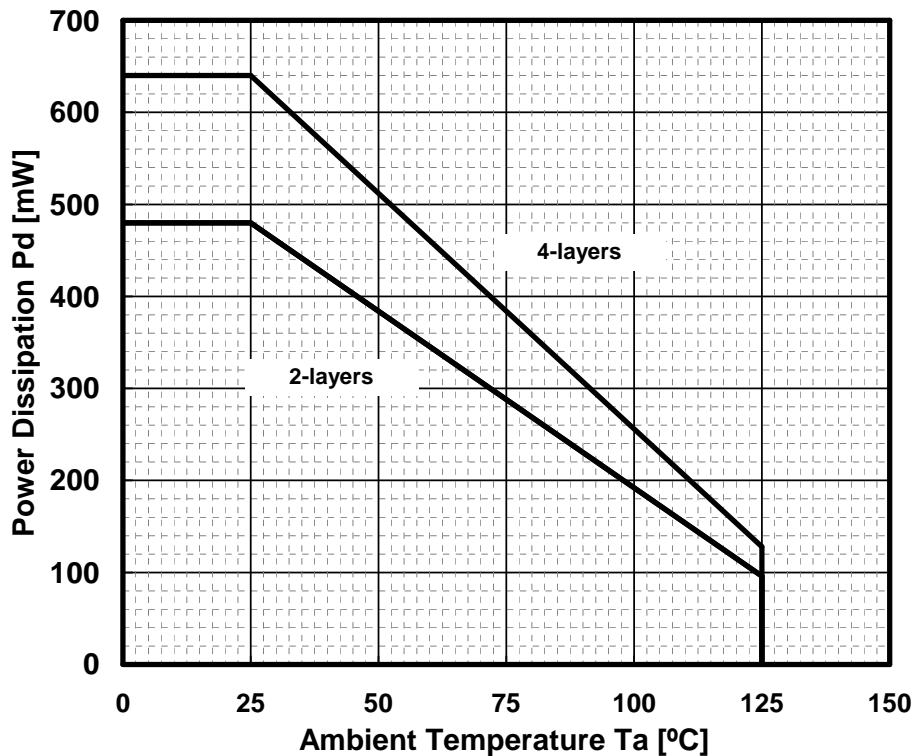
For supply voltage less than +7V, the absolute maximum rating is equal to the supply voltage.

(2) The normal operation will establish when any input is within the Common Mode Input Voltage Range of electrical characteristics.

(3) EIA/JEDEC STANDARD Test board (76.2 x 114.3 x 1.6mm, FR-4) mounting.

Do not exceed "Power dissipation: P_D" in which power dissipation in IC is shown by the absolute maximum rating. See Figure1 "Power Dissipation Curve" when ambient temperature is over 25° C.

Figure1.Power Dissipation Derating Curve



■ RECOMMENDED OPERATING CONDITION(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V ⁺ - V ⁻	+2.4 to +5.5	V

■ ELECTRICAL CHARACTERISTICS
 $V^+ = 5V, V^- = 0V, V_{COM} = 2.5V, T_a = 25^\circ C$, unless otherwise noted.

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
INPUT CHARACTERISTICS						
Input Offset Voltage	V_{IO}	$T_a = 25$		0.4	1.5	mV
		$T_a = -40$ to 125			1.8	mV
Input Offset Voltage Drift	$V_{IO}/\Delta T$	$T_a = -40$ to 125		0.5	3	$\mu V/^\circ C$
Input Bias Current	I_B			1		pA
Input Offset Current	I_{IO}			1		pA
Open Loop Gain	A_V	$R_L = 10k$ to $2.5V$	102	110		dB
Common Mode Rejection Ratio	CMR	$V_{ICM} = 0V$ to $5V$	70	92		dB
Common Mode Input Voltage Range	V_{ICM}	CMR 70dB	0		5	V
Common Mode Input Resistance	R_{ICM}			1000		Ω
Differential Mode Input Resistance	R_{IDM}			1000		Ω
Input Capacitance	C_{IN}			11		pF
OUTPUT CHARACTERISTICS						
High-level Output Voltage	V_{OH}	$R_L = 10k$ to $2.5V$	4.95	4.99		V
		$R_L = 600$ to $2.5V$	4.90	4.95		V
Low-level Output Voltage	V_{OL}	$R_L = 10k$ to $2.5V$		7	40	mV
		$R_L = 600$ to $2.5V$		35	80	mV
Short-circuit Output Current	I_{SC}	Short to V^+	70	110		mA
		Short to V^-	70	110		
Output Resistance	R_O			95		Ω
AC CHARACTERISTICS						
Gain Bandwidth Product	GBP	$G_V = 60dB, R_S = 500$, $R_L = 10k\Omega$ to $2.5V$, $C_L = 20pF$, $f = 1MHz$		34		MHz
Phase Margin	M	$G_V = 14dB, R_S = 500$, $R_L = 10k\Omega$ to $2.5V$, $C_L = 20pF$		60		deg
Gain Margin	G_M	$G_V = 14dB, R_S = 500$, $R_L = 10k\Omega$ to $2.5V$, $C_L = 20pF$		20		dB
Slew Rate	SR	$G_V = 14dB$ (Non-Inverting Amplifier) $R_S = 500$, $R_F = 2k$, $C_L = 20pF$, $V_{IN} = 0.4V_{pp}$	18	35		V/ μs
Equivalent Input Noise Voltage	e_n	$f = 1kHz$		6		nV/ \sqrt{Hz}
		$f = 100kHz$		5		
Total Harmonic Distortion + Noise	THD	$G_V = 14dB$ (Non-Inverting Amplifier) $R_S = 500$, $R_F = 2k$, $V_O = 2V_{pp}, f = 1kHz$		0.0016		%
POWER SUPPLY						
Supply Voltage Rejection Ratio	SVR	$V^+ = 2.4V$ to $5.5V$	78	98		dB
Supply Current	I_{SUPPLY}	No Signal		3.8	4.5	mA

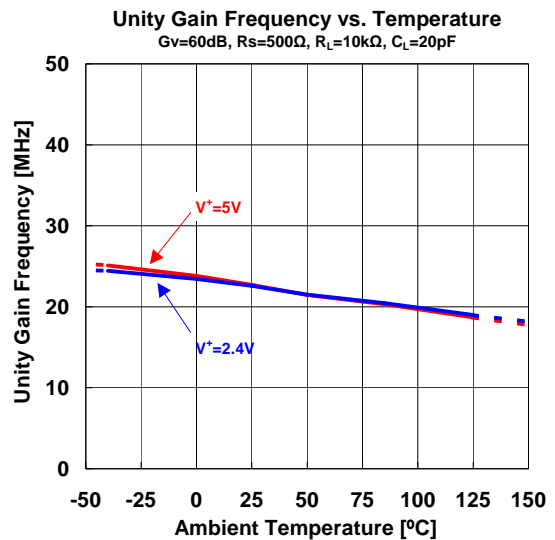
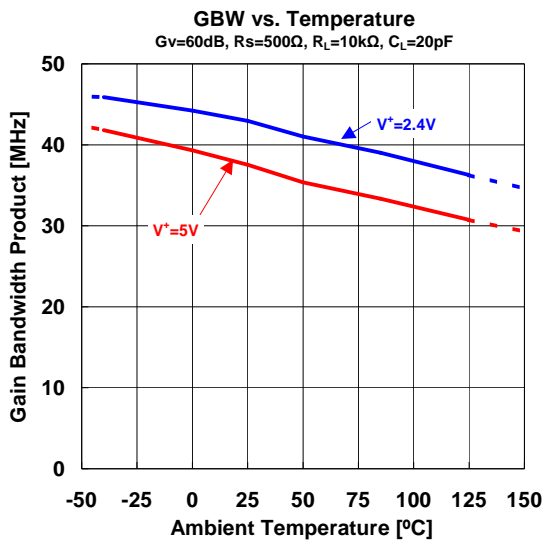
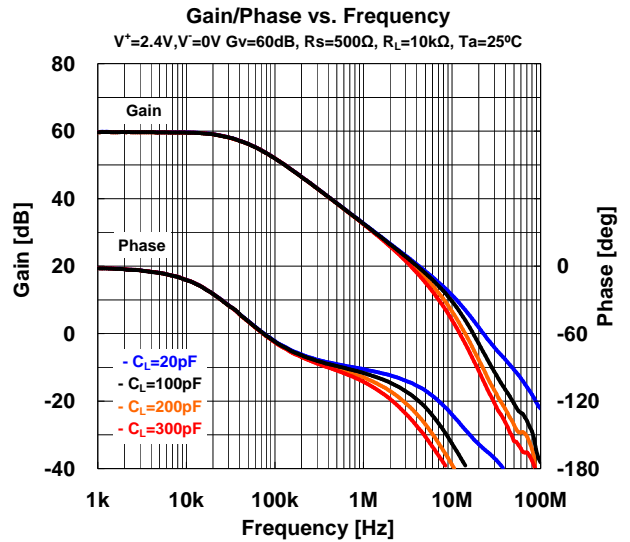
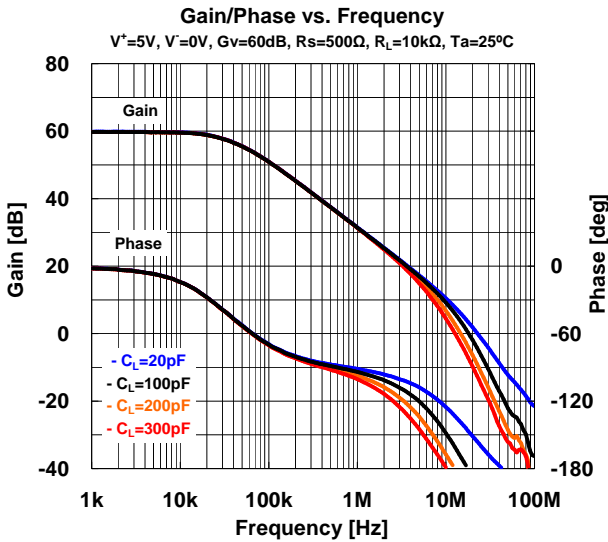
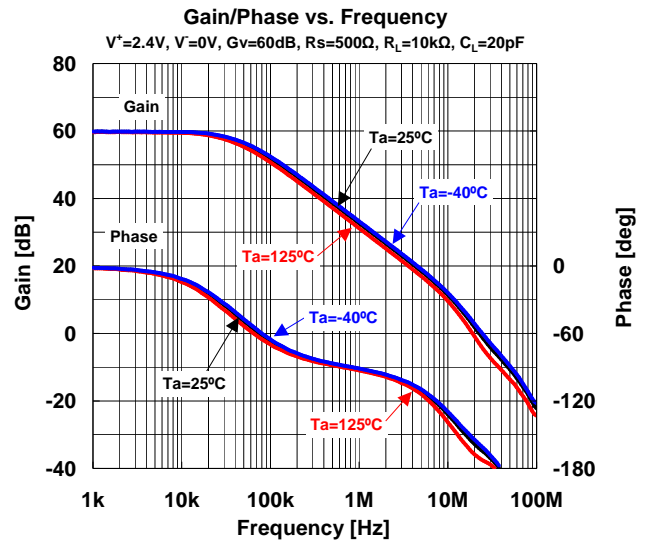
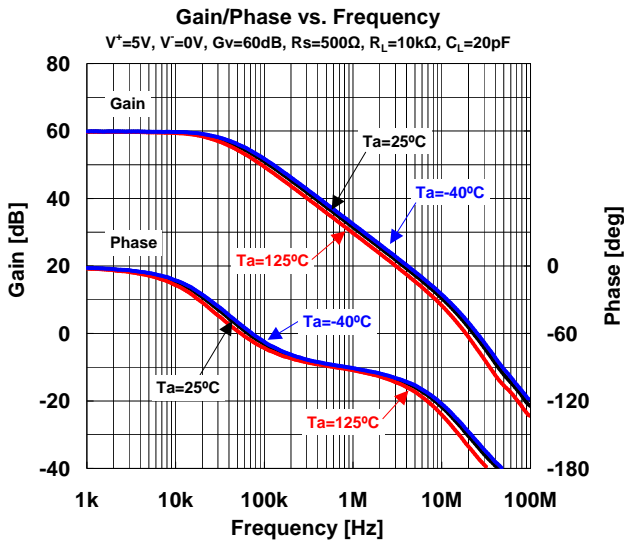
(Note) Applied circuit voltage gain is desired to operate above 14dB(5V/V).

$V^+ = 2.4V, V^- = 0V, V_{COM} = 1.2V, T_a = 25^\circ C$, unless otherwise noted.

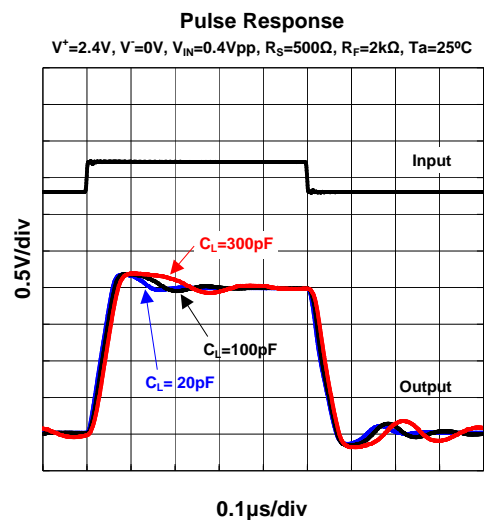
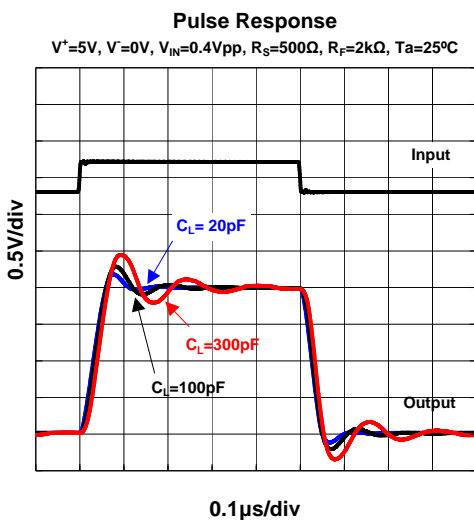
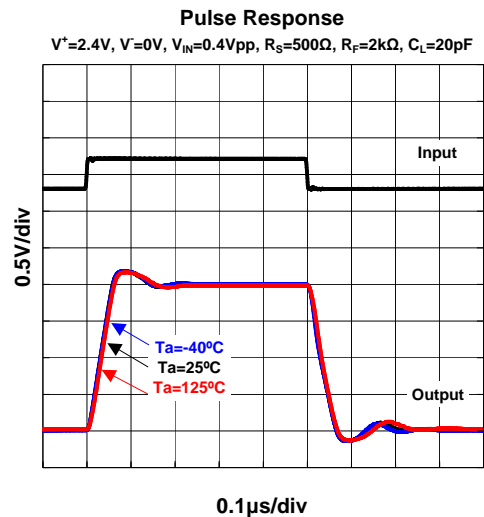
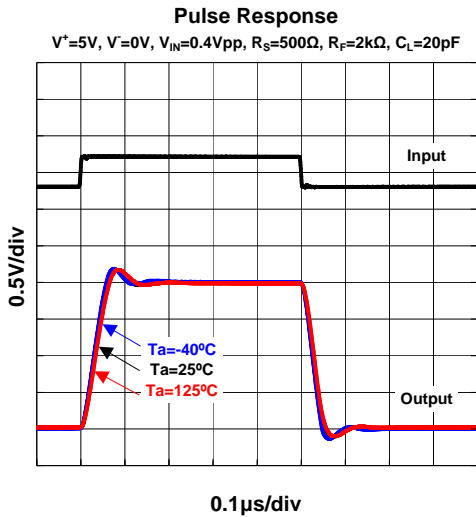
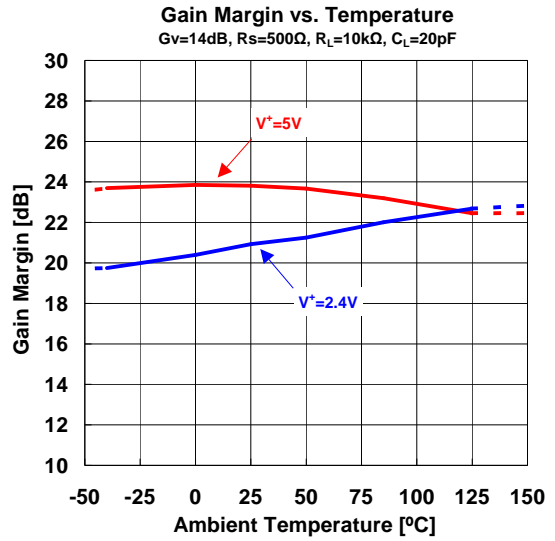
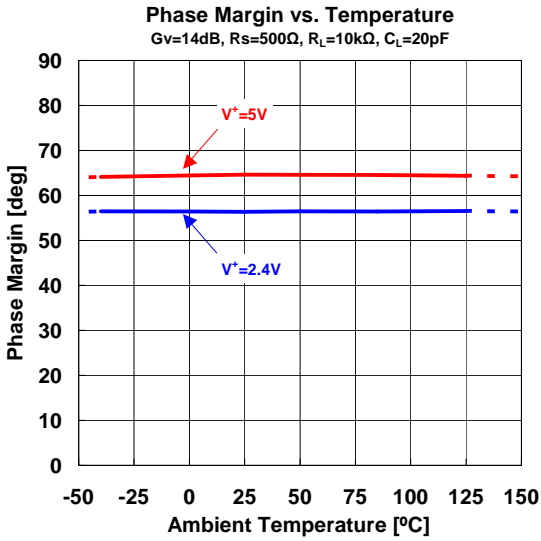
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
INPUT CHARACTERISTICS						
Input Offset Voltage	V_{IO}	$T_a = 25$		0.4	1.5	mV
		$T_a = -40$ to 125			1.8	mV
Input Offset Voltage Drift	$V_{IO}/\Delta T$	$T_a = -40$ to 125		0.7	3.5	$\mu V/$
Input Bias Current	I_B			1		pA
Input Offset Current	I_{IO}			1		pA
Open Loop Gain	A_V	$R_L = 10k$ to $1.2V$	100	110		dB
Common Mode Rejection Ratio	CMR	$V_{ICM} = 0V$ to $1.2V$	63	86		dB
Common Mode Input Voltage Range	V_{ICM}	CMR 63dB	0		2.4	V
Common Mode Input Resistance	R_{ICM}			1000		G
Differential Mode Input Resistance	R_{IDM}			1000		G
Input Capacitance	C_{IN}			11		pF
OUTPUT CHARACTERISTICS						
High-level Output Voltage	V_{OH}	$R_L = 10k$ to $1.2V$	2.35	2.40		V
		$R_L = 600$ to $1.2V$	2.32	2.38		V
Low-level Output Voltage	V_{OL}	$R_L = 10k$ to $1.2V$		1.5	40	mV
		$R_L = 600$ to $1.2V$		16	60	mV
Short-circuit Output Current	I_{SC}	Short to V^+	60	80		mA
		Short to V^-	35	60		
Output Resistance	R_O			110		
AC CHARACTERISTICS						
Gain Bandwidth Product	GBP	$G_v = 60dB, R_s = 500, R_L = 10k\Omega$ to $1.2V, C_L = 20pF, f = 1MHz$		40		MHz
Phase Margin	M	$G_v = 14dB, R_s = 500, R_L = 10k\Omega$ to $1.2V, C_L = 20pF$		60		deg
Gain Margin	G_M	$G_v = 14dB, R_s = 500, R_L = 10k\Omega$ to $1.2V, C_L = 20pF$		20		dB
Slew Rate	SR	$G_v = 14dB$ (Non-Inverting Amplifier) $R_s = 500, R_f = 2k, C_L = 20pF, V_{IN} = 0.4V_{pp}$	17	31		V/us
Equivalent Input Noise Voltage	e_n	$f = 1kHz$		6		nV/ \sqrt{Hz}
		$f = 100kHz$		5		
Total Harmonic Distortion + Noise	THD	$G_v = 14dB$ (Non-Inverting Amplifier) $R_s = 500, R_f = 2k, V_o = 2V_{pp}, f = 1kHz$		0.01		%
POWER SUPPLY						
Supply Voltage Rejection Ratio	SVR	$V^+ = 2.4V$ to $5.5V$	78	98		dB
Supply Current	I_{CC}	No Signal		3.4	4.1	mA

(Note) Applied circuit voltage gain is desired to operate above 14dB(5V/ V).

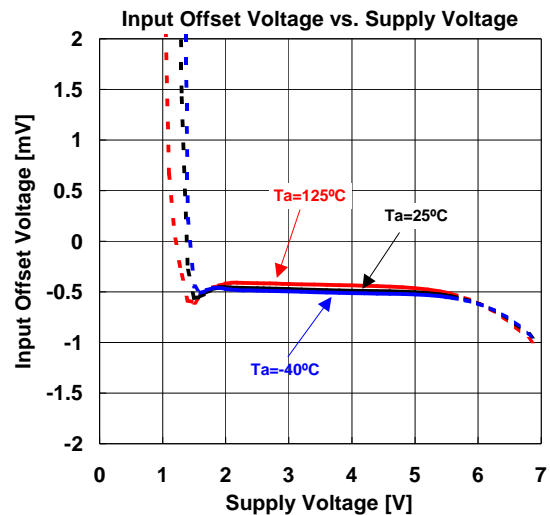
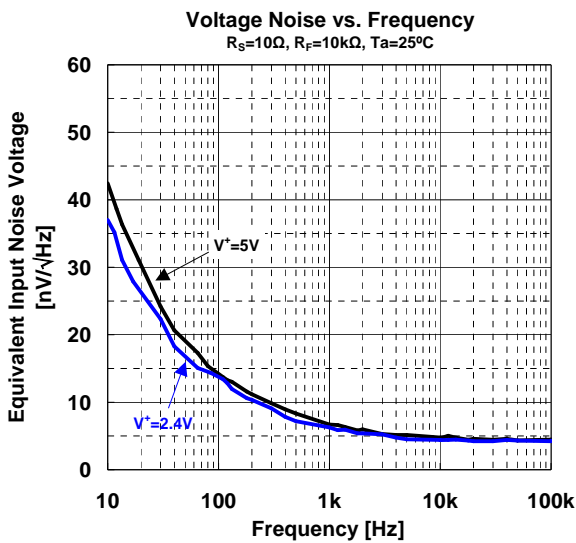
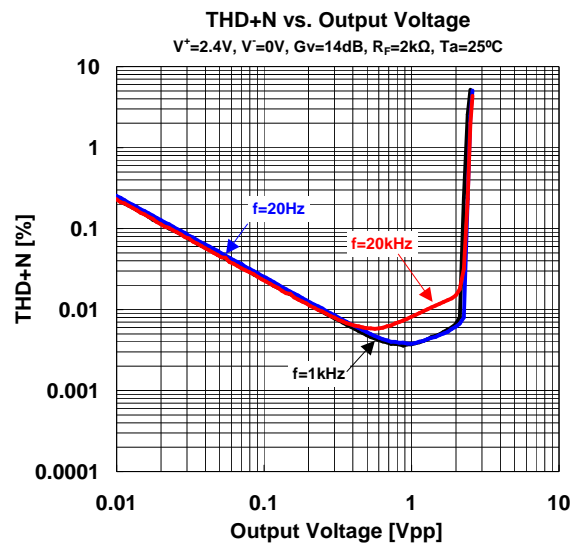
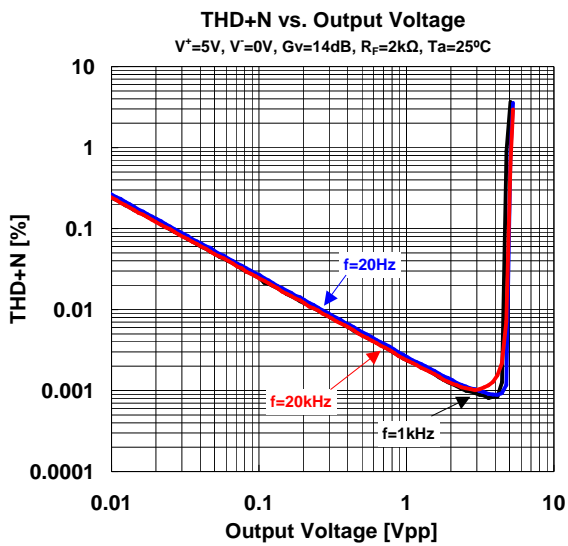
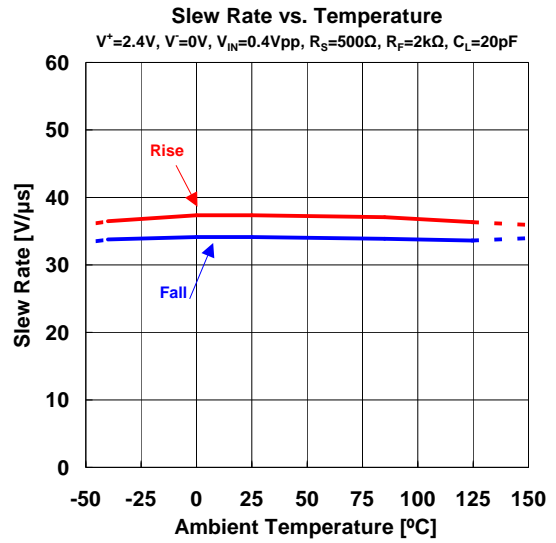
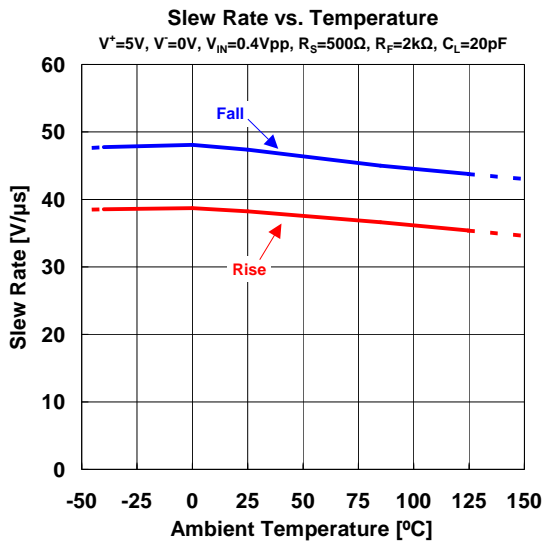
■ TYPICAL CHARACTERISTICS



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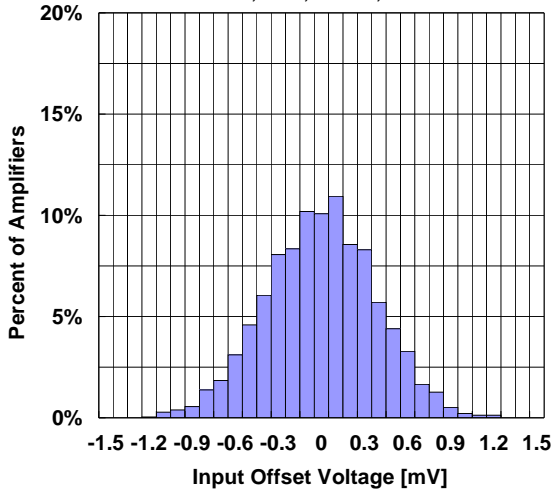


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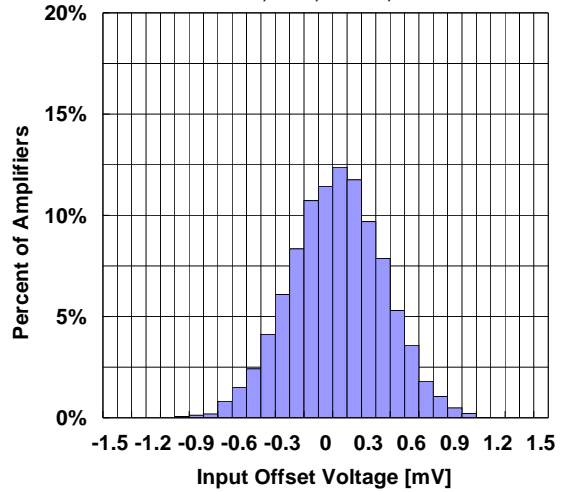


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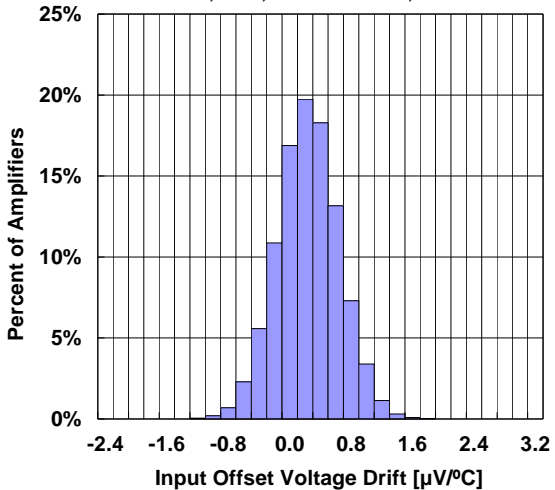
Input Offset Voltage Distribution
 $V^+=5V, V^-=0V, T_a=25^\circ C, n=4000$



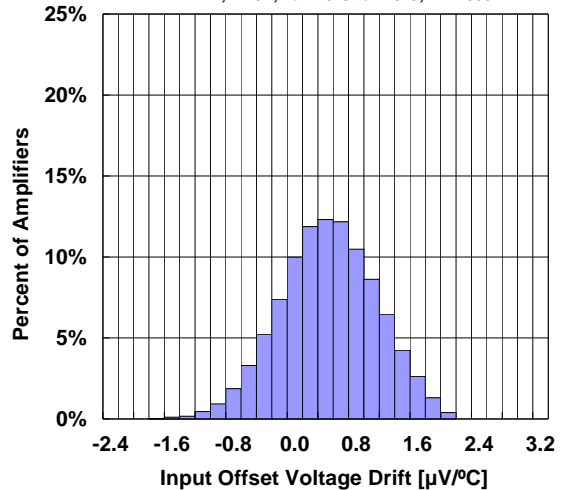
Input Offset Voltage Distribution
 $V^+=2.4V, V^-=0V, T_a=25^\circ C, n=4000$



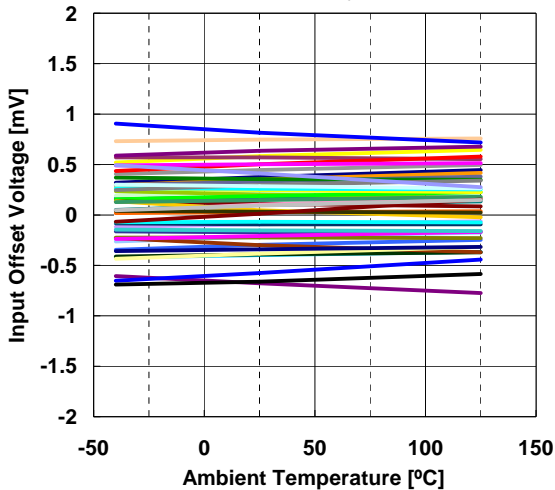
Input Offset Voltage Drift Distribution
 $V^+=5V, V^-=0V, T_a=-40^\circ C \text{ to } 125^\circ C, n=27000$



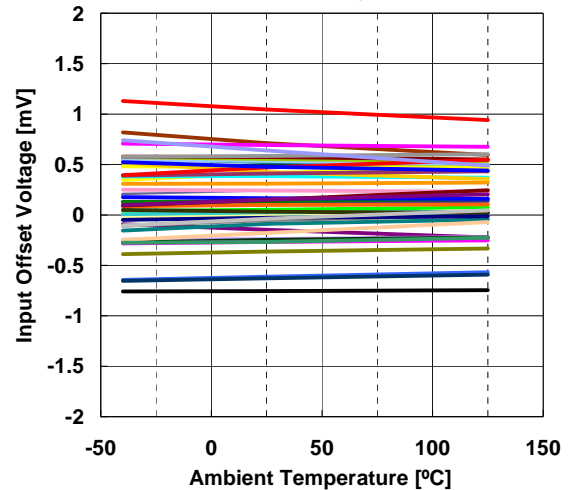
Input Offset Voltage Drift Distribution
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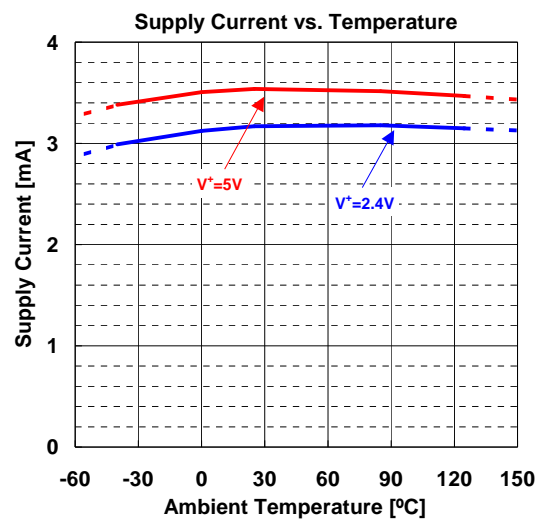
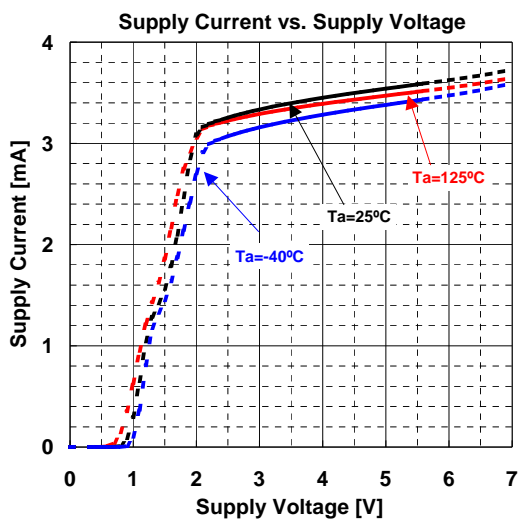
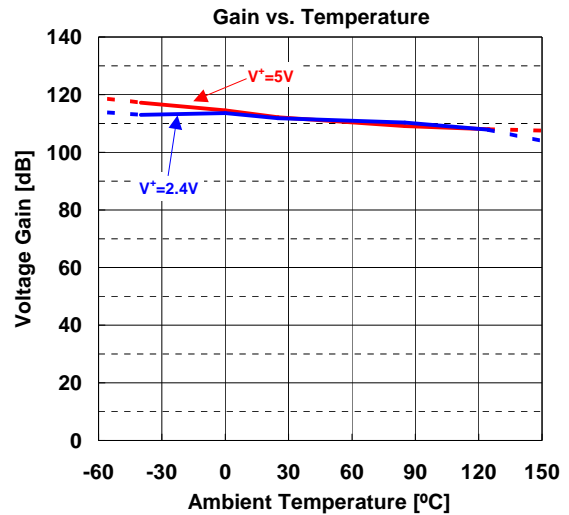
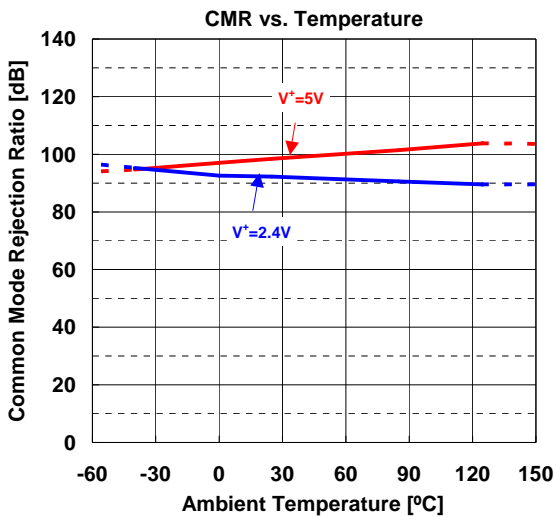
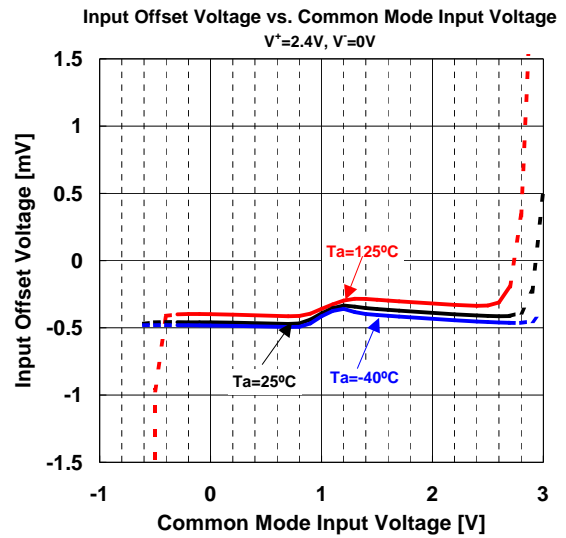
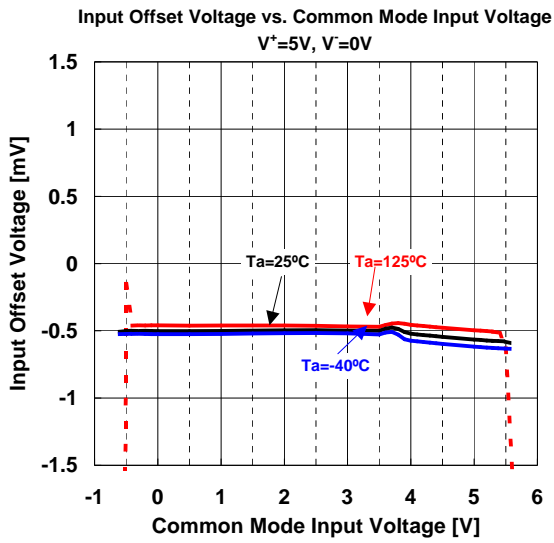
Input Offset Voltage vs. Temperature
 $N=48, V^+=5V, V^-=0V, V_{ICM}=2.5V$



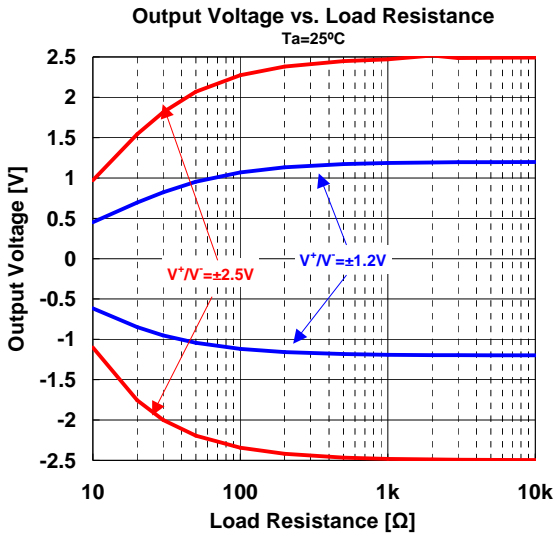
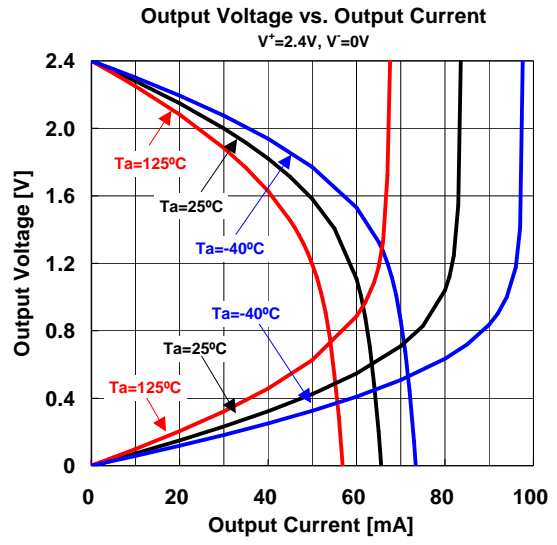
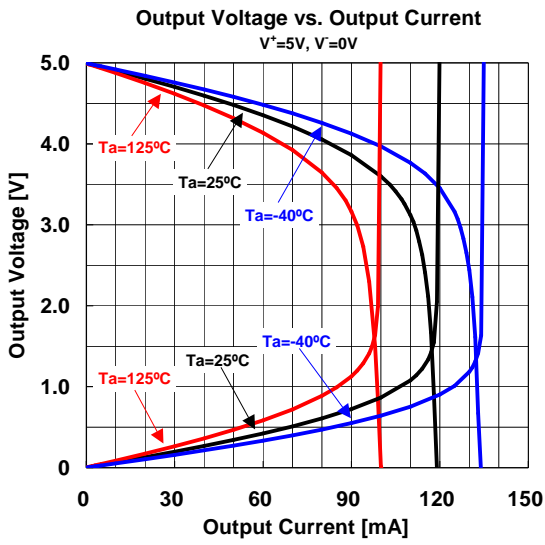
Input Offset Voltage vs. Temperature
 $N=48, V^+=2.4V, V^-=0V, V_{ICM}=1.2V$



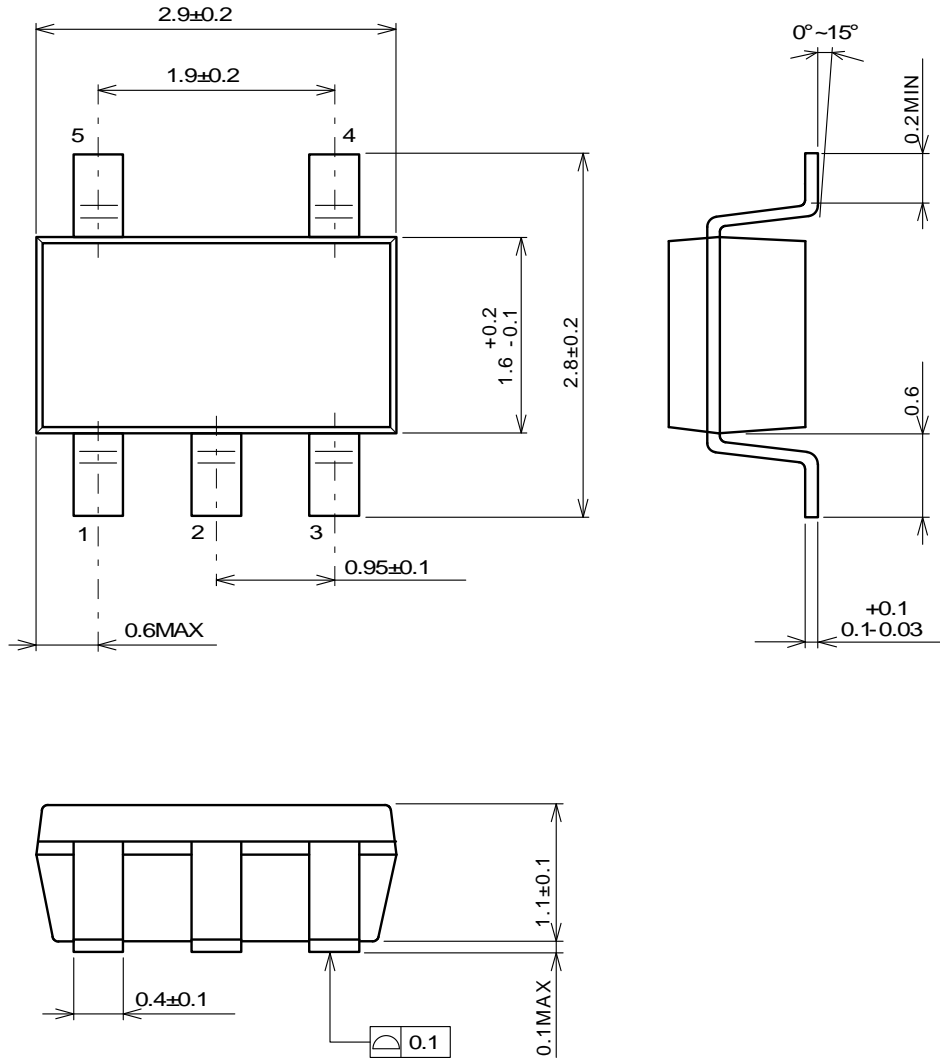
■ TYPICAL CHARACTERISTICS



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■ PACKAGE DIMENSIONS



[CAUTION]
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