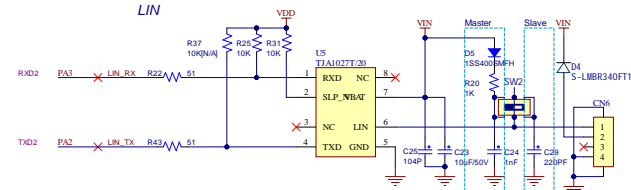
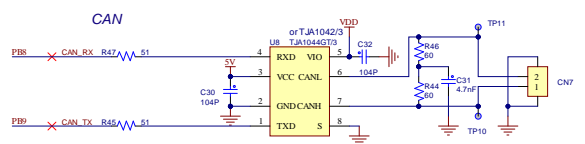
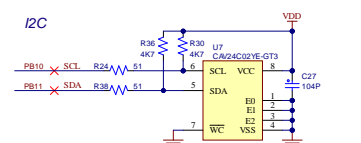
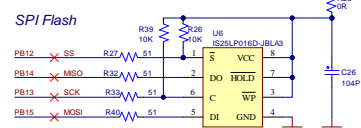
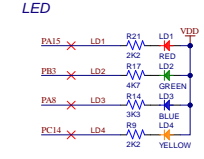
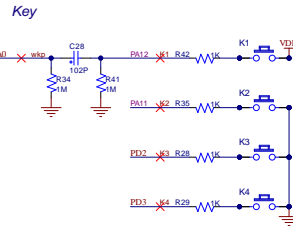
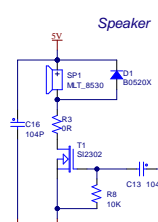
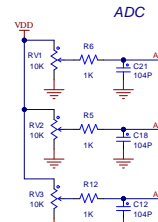
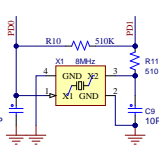
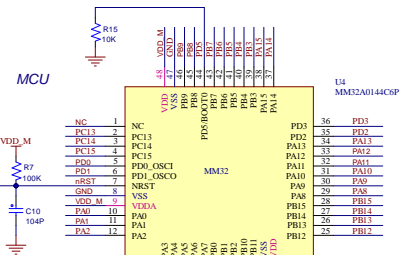


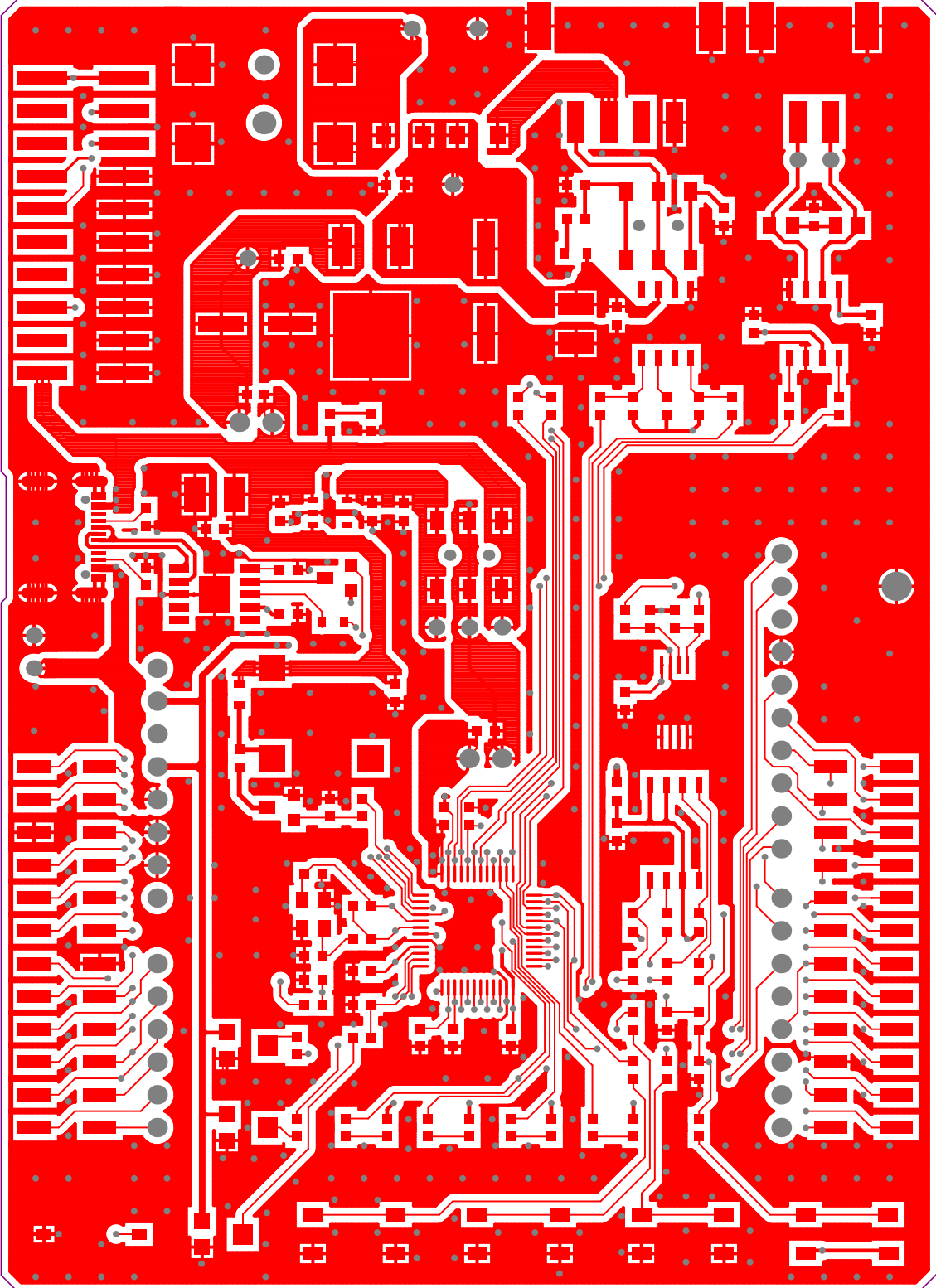
PA0/WKUP/UART2_CTS/TM2_CH1/TM2_ETR/SP2_NSS/2S2_WS/TM2_CH3/COMP1_OUT/ADC1_VN[0]
 PA1/UART2_RTS/TM2_CH2/ADC1_VN[1]/COMP_RP[0]
 PA2/UART2_TX/TM2_CH3/SP2_NSS/2S2_WS/ADC1_VN[2]/COMP_INF[1]
 PA3/UART2_RX/TM2_CH4/ADC1_VN[3]/COMP_INF[2]
 PA4/SP1_NSS/2S1_WS/TM1_BKN/TM1_4_CH1/2C_SDA/ADC1_VN[4]/COMP_INF[3]
 PA5/SP1_SCK/2S1_CK/TM2_CH1/TM2_ETR/TM1_ETR/2C_SCL/TM1_CHN/ADC1_VN[5]/COMP_INF[0]
 PA6/SP1_MSO/2S1_MCK/TM2_CH1/TM1_BKN/UART2_RX/TM1_ETR/TM1_6_CH1/TM1_CH3/COMP1_OUT/ADC1_VN[6]/COMP_INF[1]
 PA7/SP1_MOSI/2S1_SD/TM2_CH2/TM1_CH1/TM1_4_CH1/TM1_7_CH1/TM1_CH2/TM1_CHN/ADC1_VN[7]/COMP_INF[2]
 PA8/MCO/TM1_CH1/TM1_CH2/TM1_CH3
 PA9/UART1_TX/TM1_CH2/UART1_RX/2C_SCL/MCO/TM1_CH1/TM1_CH1CAN_RX
 PA10/TM1_BKN/UART1_RX/TM1_CH3/UART1_TX/2C_SDA/TM1_CH1/SP2_SCK/2S2_CK/CAN_TX
 PA11/UART3_TX/UART1_CTS/TM1_CH4/CAN_RX/SP2_MSO/2S2_SD/2C_SCL/COMP1_OUT
 PA12/UART3_RX/UART1_RTS/TM1_ETR/CAN_TX/SP2_MSO/2S2_MCK/2C_SDA/TM1_CH2
 PA13/SWCLK/UART1_TX/SP2_MSO/2S2_MCK/MCO/TM1_CH2/TM1_BKN
 PA14/SWCLK/UART2_TX/UART1_RX/SP1_NSS/2S1_WS
 PA15/SP1_NSS/2S1_WS/UART2_RX/TM2_CH1/TM2_ETR
 PB0/TM3_CH3/TM1_CH2/TM1_CH1/TM1_CH3/ADC1_VN[8]
 PB1/TM1_4_CH1/TM2_CH1/TM1_CHN/ADC1_VN[9]
 PB2
 PB3/SP1_SCK/2S1_CK/TM2_CH3/UART1_TX/TM2_CH1/TM1_CH1/TM1_CH1/ADC1_VN[10]
 PB4/SP1_MSO/2S1_MCK/TM2_CH1/UART1_RX/TM1_7_BKN/TM1_CH2/TM2_CH2/ADC1_VN[11]
 PB5/SP1_MSO/2S1_SD/TM3_CH2/TM1_6_BKN/MCO/TM1_CH1/TM2_CH3
 PB6/UART1_TX/2C_SCL/TM1_6_CH1/TM2_CH1
 PB7/UART1_RX/2C_SDA/TM1_7_CH1/UART2_TX/ADC1_VN[12]
 PB8/2C_SCL/TM1_6_CH1/CAN_RX/UART2_RX
 PB9/2C_SDA/TM1_7_CH1/CAN_TX/TM1_CH4/SP2_NSS/2S2_WS
 PB10/2C_SCL/TM2_CH3/UART3_TX/SP2_SCK/2S2_CK
 PB11/2C_SDA/TM2_CH4/UART3_RX
 PB12/SP2_NSS/2S2_WS/SP2_SCK/2S2_CK/TM1_BKN/SP2_MSO/2S2_SD/SP2_MSO/2S2_MCK
 PB13/SP2_SCK/2S2_CK/SP2_MSO/2S2_MCK/TM1_CH1/NSP2_MSO/2S2_SD/SP2_MSO/2S2_MCK
 PB14/SP2_MSO/2S2_MCK/SP2_MSO/2S2_SD/TM1_CH1/NSP2_SCK/2S2_CK/SP2_NSS/2S2_WS/2C_SDA/TM1_CH1/UART3_RTS
 PB15/SP2_MSO/2S2_SD/SP2_NSS/2S2_WS/TM1_CHN/SP2_MSO/2S2_MCK/SP2_SCK/2S2_CK/TM1_CH2/TM1_CH2

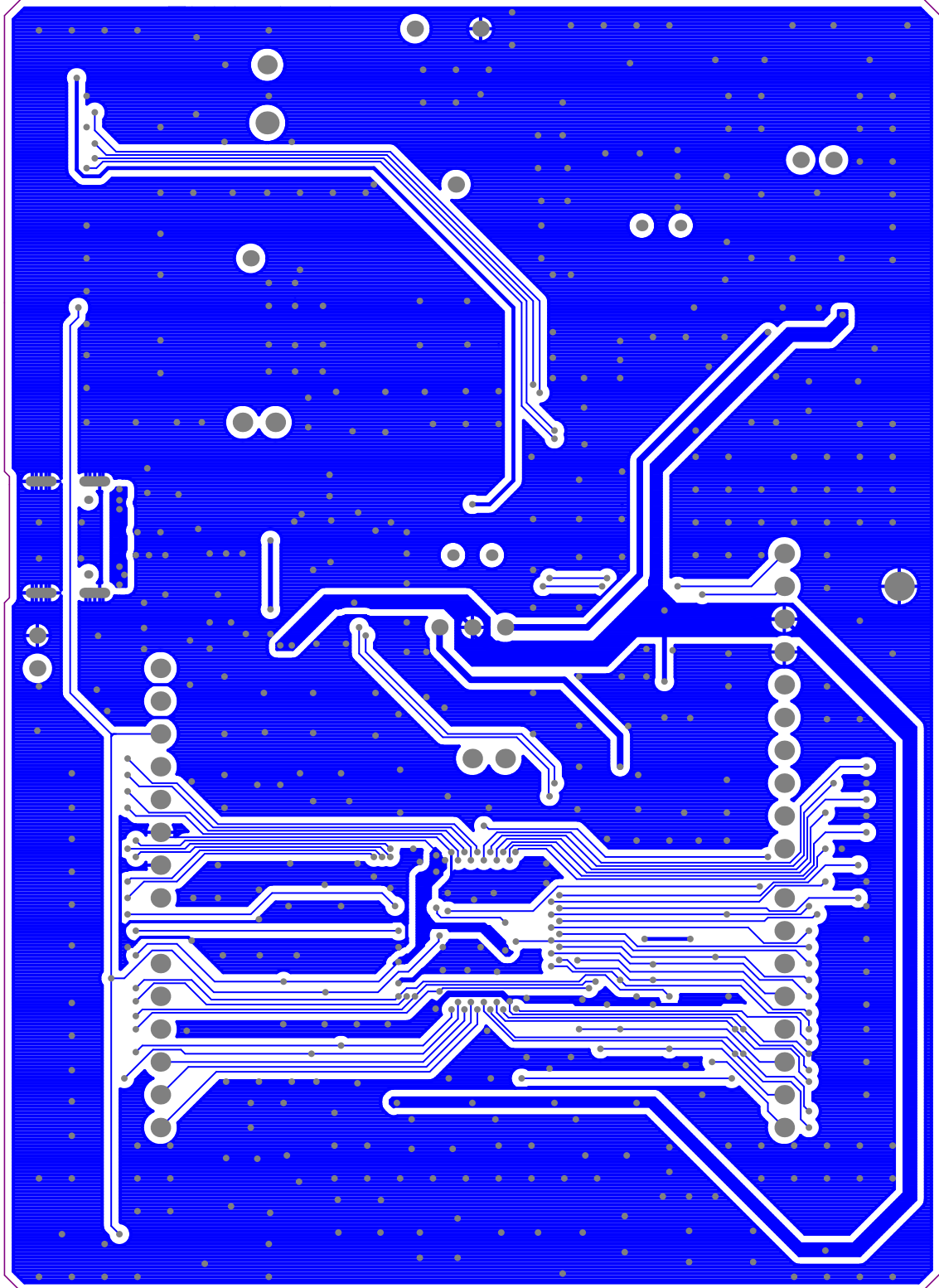
PC13/TM2_CH1
 PC14/TM2_CH2
 PC15/TM2_CH3

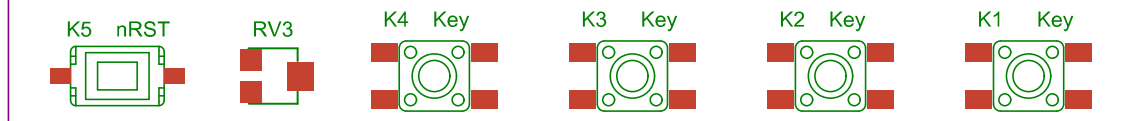
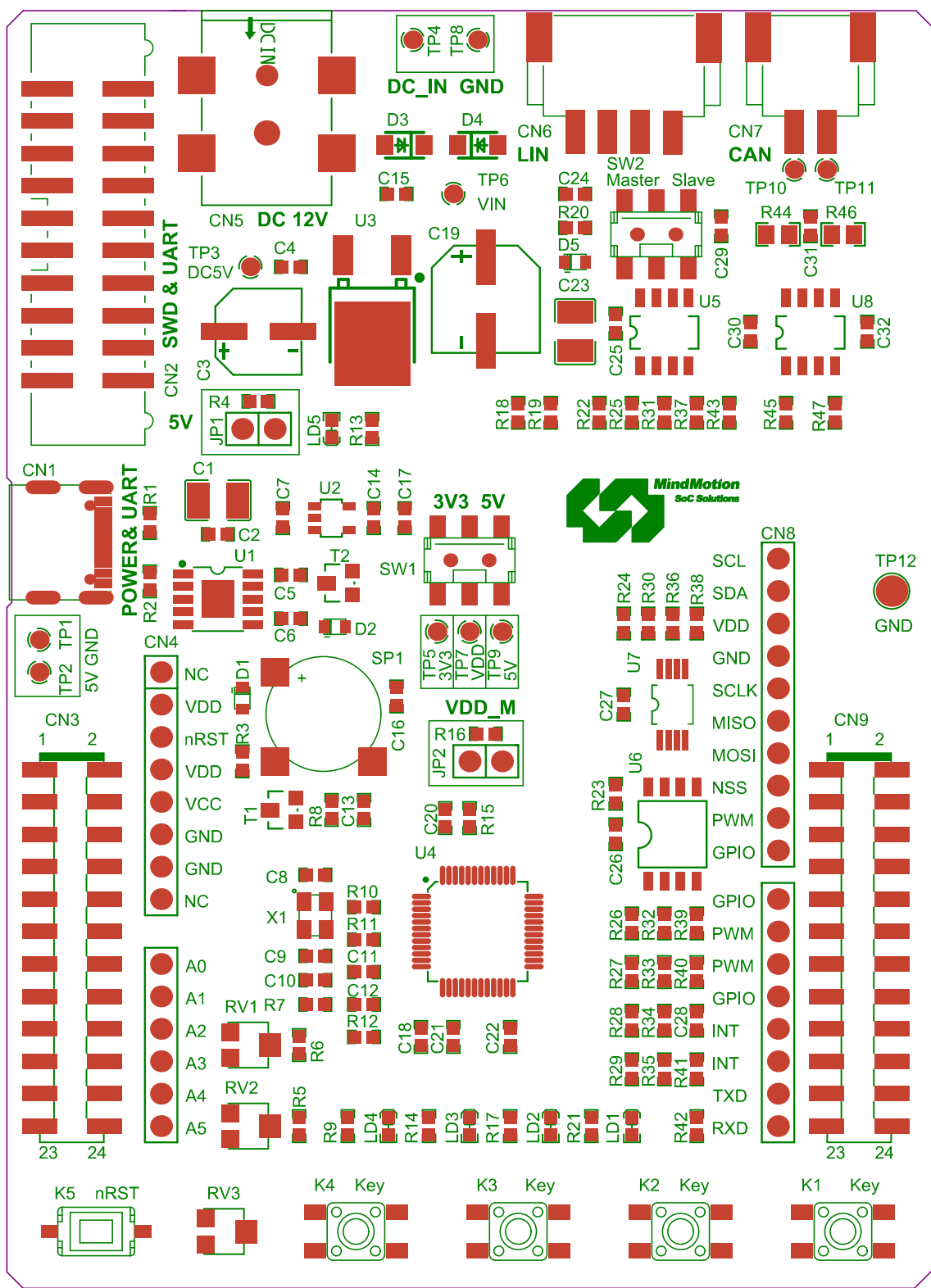
PD0/OSC_IN/UART3_TX/2C_SDA
 PD1/OSC_OUT/UART3_RX/2C_SCL
 PD2
 PD3
 PD3/BOOT0



REVA更新:
 1)DC/DC切换为LDO
 2)LIN的Master/Slave增加了切换开关







CANL
CANH

GND
NC
VIN
LIN



MindMotion
SoC Solutions

MM32 MiniBoard

			PB10
			PB11
			VDD
			GND
			PB3
			PB4
			PB5
2	1		PA15
PB6	PB5		PB1
PB4	PB3		PB2
PA15	PA14		
PD3	PD2		PD5
PA13	PA12		PC15
PA11	PA10		PC14
PA9	PA8		PD3
PB15	PB14		PA8
PB13	PB12		PD2
VDD_M	GND		PA11
PB11	PB10		PA12
PB2	PB1		
24	23		

MB-082
Rev.A

Mini-A0144
MM32A0144C6P

NC			
VDD			
nRST			
VDD	2	1	
VCC	PD5	PB7	
GND	PB9	PB8	
GND	VDD_M	GND	
NC	PC13	NC	
	PC15	PC14	
	PD1	PD0	
PA1	GND	nRST	
PA4	PA0	VDD_M	
PA5	PA2	PA1	
PA6	PA4	PA3	
PA7	PA6	PA5	
PB0	PB0	PA7	
	24	23	